

Refine Search

Your wildcard search against 10000 terms has yielded the results below.

Your result set for the last L# is incomplete.

The probable cause is use of unlimited truncation. Revise your search strategy to use limited truncation.

Search Results

| Terms | Documents |
|---|-----------|
| L8 and (variab\$ or data\$ or persistent\$ or value\$ or key\$ or idnetif\$ or arbitar\$) | 1 |

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US Patents OCR Backfile
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletin Database

Search Type: Prior Art Interference

Search:

Search History

DATE: Monday, July 12, 2010 [Purge Queries](#) [Printable Copy](#) [Create Case](#)

| <u>Set Name</u> | <u>Query</u> | | <u>Hit Count</u> | <u>Set Name</u> | <u>Set Name Grid</u> |
|-----------------|--------------|--|------------------|-----------------|----------------------|
| Side by Side | | | Result Set | | |

Prior Art Searches

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

| | | | | |
|------------|---|------|------------|------------|
| <u>L30</u> | 18 and (variab\$ or data\$ or persistent\$ or value\$ or key\$ or idnetif\$ or arbitar\$) | 1 | <u>L30</u> | <u>L30</u> |
| <u>L29</u> | (firmwar\$ near6 variabl\$) near7 compress\$ | 7 | <u>L29</u> | <u>L29</u> |
| <u>L28</u> | (firmwar\$ near6 variabl\$) | 802 | <u>L28</u> | <u>L28</u> |
| <u>L27</u> | L26 and variabl\$ | 2 | <u>L27</u> | <u>L27</u> |
| <u>L26</u> | 20070033322 | 2 | <u>L26</u> | <u>L26</u> |
| <u>L25</u> | L24 and (stor\$ or sav\$ Or memory\$ or firmwar\$ or bios\$) near4 compress\$ | 8 | <u>L25</u> | <u>L25</u> |
| <u>L24</u> | L23 and (variabl\$ near4 (firmwar\$ or bios\$)) | 13 | <u>L24</u> | <u>L24</u> |
| <u>L23</u> | (compress\$ near4 variabl\$) and (firmwar\$ or bios\$) | 1326 | <u>L23</u> | <u>L23</u> |
| <u>L22</u> | 5867712.pn. | 2 | <u>L22</u> | <u>L22</u> |

| | | | | |
|------------|---|---|------------|------------|
| <u>L21</u> | 5592640.pn. | 2 | <u>L21</u> | <u>L21</u> |
| <u>L20</u> | 4135242.pn. | 5 | <u>L20</u> | <u>L20</u> |
| <u>L19</u> | 5706460.pn. | 2 | <u>L19</u> | <u>L19</u> |
| <u>L18</u> | 5706460.pn. | 2 | <u>L18</u> | <u>L18</u> |
| <u>L17</u> | L16 and (firmwar\$ or bios\$) and variabl\$ and comprss\$ | 0 | <u>L17</u> | <u>L17</u> |
| <u>L16</u> | 5592640.pn. | 2 | <u>L16</u> | <u>L16</u> |
| <u>L15</u> | L14 and variabl\$ | 1 | <u>L15</u> | <u>L15</u> |
| <u>L14</u> | L13 and compress\$ | 2 | <u>L14</u> | <u>L14</u> |
| <u>L13</u> | 5729668.pn. | 2 | <u>L13</u> | <u>L13</u> |
| <u>L12</u> | L11 and compress\$ | 0 | <u>L12</u> | <u>L12</u> |
| <u>L11</u> | 4528644.pn. | 2 | <u>L11</u> | <u>L11</u> |
| <u>L10</u> | l8 and variabl\$ | 0 | <u>L10</u> | <u>L10</u> |
| <u>L9</u> | L8 and compress\$ | 2 | <u>L9</u> | <u>L9</u> |
| <u>L8</u> | 5901310.pn. | 2 | <u>L8</u> | <u>L8</u> |
| <u>L7</u> | L6 and compress\$ | 1 | <u>L7</u> | <u>L7</u> |
| <u>L6</u> | 5999989.pn. | 2 | <u>L6</u> | <u>L6</u> |
| <u>L5</u> | 7188238.pn. | 2 | <u>L5</u> | <u>L5</u> |
| <u>L4</u> | 7260848.pn. | 2 | <u>L4</u> | <u>L4</u> |
| <u>L3</u> | 7254676.pn. | 2 | <u>L3</u> | <u>L3</u> |
| <u>L2</u> | 7080246.pn. | 2 | <u>L2</u> | <u>L2</u> |
| <u>L1</u> | 7222258.pn. | 2 | <u>L1</u> | <u>L1</u> |

END OF SEARCH HISTORY